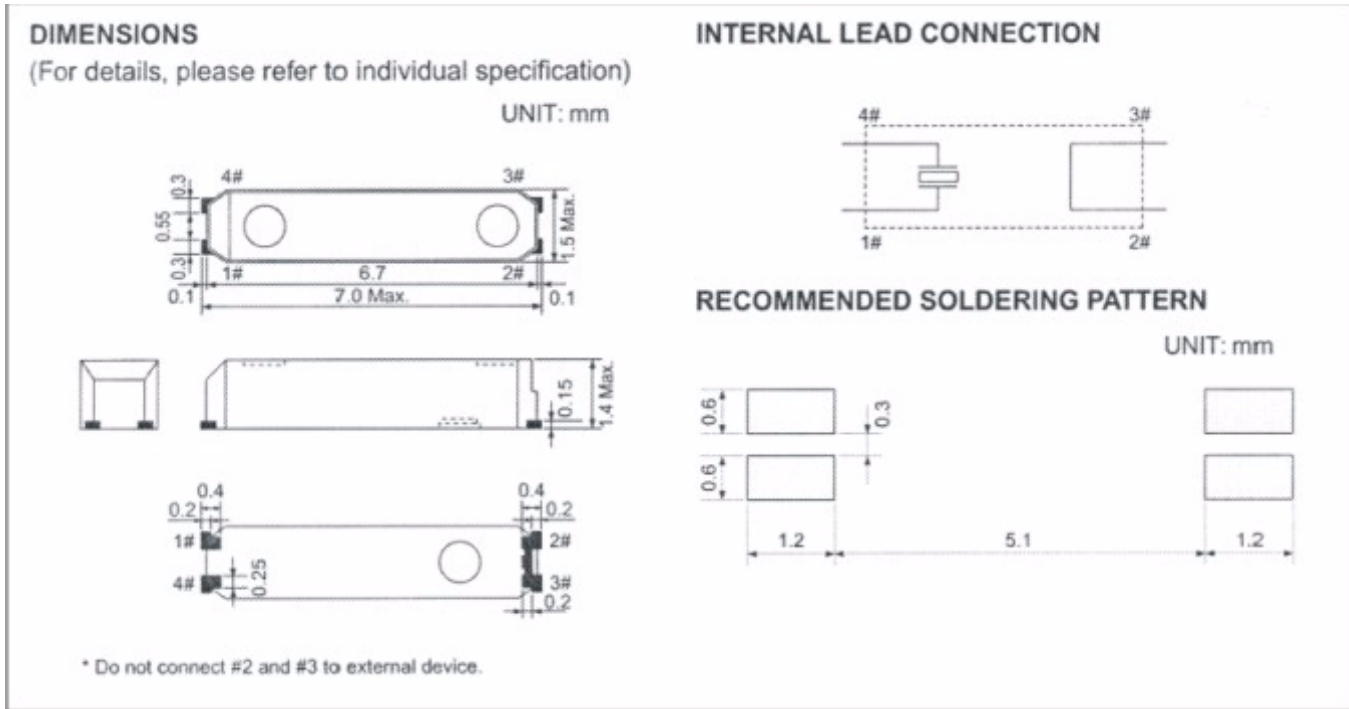




SPECIFICATION CONTROL DRAWING Crystal Resonator

FREQUENCY
32.768000kHz



Holder Style	7x1,5 SMD
Mode	Fundamental
Adjustment Tolerance	As required by CSR Chipset
Temperature Stability	$\pm -.034$ PPM /°C ²
Operating Temp. Range	-40°C to +85°C
Load Capacitance	12.5 pF
ESR	As required by CSR Chipset
Drive Level	1.0uW Max mW
Static Capacitance (C0)	As required by CSR Chipset
Dynamic Capacitance (C1)	As required by CSR Chipset
C0/C1 Ratio	As required by CSR Chipset
Q Factor	As required by CSR Chipset
Ageing	+/- 3ppm /Yr Max
Marking	Mark Std

Amdt Date	Change Note	AEL Code		
27 AUG 09	Additional Detail added	X32K768S019		
		<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 70%; text-align: center;">Issue Ref</td> <td style="text-align: center;">B</td> </tr> </table>	Issue Ref	B
Issue Ref	B			