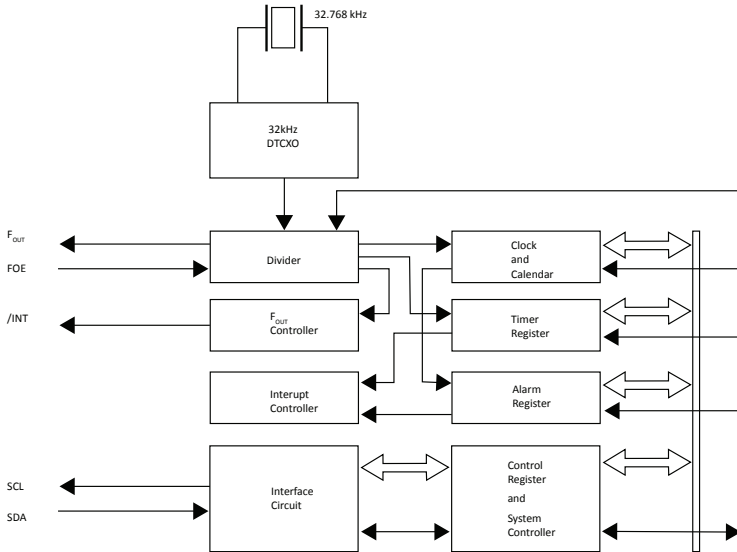


### Block Diagram

### Overview

## High Stability I<sup>2</sup>C-Bus Interface<sup>®</sup> – 32.768kHz DTCXO

I<sup>2</sup>C Bus is registered to NXP Semiconductors



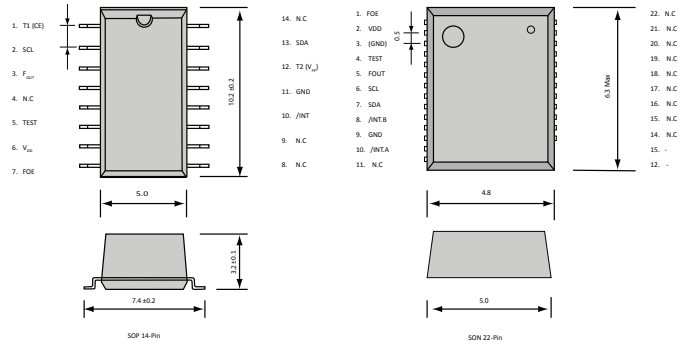
### High Stability

- **UA**  
 $\pm 1.9 \times 10^{-6}$  over 0°C to +40°C  
 (Equivalent to 5 secs/month drift)  
 $\pm 3.5 \times 10^{-6}$  over -40°C to +85°C  
 (Equivalent to 9 secs/month drift)
- **UB**  
 $\pm 3.8 \times 10^{-6}$  over 0°C to +50°C  
 (Equivalent to 10 secs/month drift)  
 $\pm 5.0 \times 10^{-6}$  over -40°C to +85°C  
 (Equivalent to 13 secs/month drift)
- **32.768kHz Frequency Output Function**
  - FOUT pin output (Cmos O/P) CL=30pF
  - Output selectable  
 < 32.768kHz, 1024 Hz, 1 Hz
- **The various interrupt**
  - Timer function can be set up between 1/4096 seconds and 255 minutes
  - Alarm function can be set to day of the week, day, hour or minute

### Pin Function

### Terminal Connections

Pin Name	Pin N <sup>o</sup>	I/O	Function
T1(CE)	1	Input	Manufacturers test pin - Do not connect
SCL	2	Input	Serial Clock Input Pin
F <sub>OUT</sub>	3	Output	Output Reference Clock Signal (Cmos O/P)
Test	4	Input	Manufacturers test pin - Do not connect
V <sub>DD</sub>	5	-	Connected to +ve poer supply
F <sub>OE</sub>	6	Input	Input pin for FOUT control
/INT	10	Output	Interrupt O/P (N-ch open drain)
GND	11	-	Connected to ground
T2(V <sub>pp</sub> )	12	-	Manufacturers test pin - Do not connect
SDA	13	I/O	Data I/O Pin



JR-8801 SA

JR-8801 JE

### Specifications

### Frequency/Temperature Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	
Interface Voltage	VDD	Interface Voltage	1.8	3.0	5.5	V	
Temp. Comp. Voltage	VTEM	Temp. Compensated Volt	2.2	3.0	5.5	V	
Clock Voltage	VCLK	-	1.6	3.0	5.5	V	
O.T.R	TOPR	-	-40	+25	+85	°C	
Stability	F/f	UA	Ta= 0°C to +40°C	±1.9		x 10 <sup>-6</sup>	
			Ta=-40°C to +85°C	±3.4			
		UB	Ta=0°C to +50°C	±3.8			
			Ta=-40°C to +85°C	±5.0			
Current Consumption	I <sub>BK1</sub>	Backup Mode FOE=GND /INT=VDD FOUT O/P: OFF	VDD=5V	-	1.2	3.4	µA
Current Consumption	I <sub>BK2</sub>		VDD=3V	-	0.8	2.8	

